

JSKD/JSMD/JSND121

Description

- 1) A package of series of two diodes.
- 2) Heat transfer through alumina ceramic and metal substrate.
- 3) Welding by vacuum welding technology, which provide high reliability.



Typical Application

AC converter, inverter and DC motor.

Absolute Maximum Ratings (Packaged into modules, unless otherwise specified, $T_{CASE}=25^{\circ}C$)

Parameter	Test Conditions	Symbol	Values				Unit
			12	16	18	20	
Operating junction temperature range		T_J	-40-150				$^{\circ}C$
Storage temperature range		T_{STG}	-40-125				$^{\circ}C$
Repetitive peak reverse voltage	$T_J=25^{\circ}C$	V_{RRM}	1200	1600	1800	2000	V
Non-repetitive peak reverse voltage	$T_J=25^{\circ}C$	V_{RSM}	1300	1700	1900	2100	V
Average on-state current	$T_C=100^{\circ}C$	$I_{F(AV)}$	120				A
Peak on-state surge current	$T_J=25^{\circ}C, t_P=10ms,$	I_{FSM}	3360				A
I^2t value for fusing	$\sin 180^{\circ}$	I^2t	56400				A^2s
Insulation voltage	A.C 50Hz(1s/1min)	V_{ISO}	3600/3000				V

Electrical Characteristics (Packaged into modules, unless otherwise specified, $T_{CASE}=25^{\circ}C$)

Parameter	Test Conditions	Symbol	Values	Unit
Peak on-state voltage	$I_F=360A, t_P=380\mu s$	V_F	≤ 1.6	V
Threshold voltage	$T_J=150^{\circ}C$	V_{TO}	≤ 0.85	V
Dynamic resistance	$T_J=150^{\circ}C$	R_d	≤ 1.5	$m\Omega$
Repetitive peak reverse current	$V_R=V_{RRM}$ $T_J=25^{\circ}C$	I_{RRM1}	≤ 100	μA
	$T_J=150^{\circ}C$	I_{RRM2}	≤ 40	mA
Thermal resistance	Junction to case	$R_{th(j-c)}$	0.29	$^{\circ}C/W$
	Case to heatsink	$R_{th(c-s)}$	0.12	

Mechanical Characteristics

Module size	93mm×21mm
Module height	30mm
Terminal distance of (1)/(2)/(3)	20mm
Mounting torque(M5)	5±15%Nm
Terminal torque(M5)	3±15%Nm
<p style="text-align: center;">T1</p>	<p style="text-align: center;">JSKD symbol</p> <p style="text-align: center;">JSMD symbol</p> <p style="text-align: center;">JSND symbol</p>

Instructions and Precautions

- 1) There is no severe vibration and shock in operating environment, and there should be no impurity and atmosphere which may corrode metal and damage the insulation in the air-dielectric.
- 2) The operating condition of the product can't out of range of the above parameters.
- 3) When the product is installed on the radiator, the radiator's surface should be confirmed flat, smooth, wipe clean with alcohol, and coated evenly with a layer of thermal grease which thickness is moderate on the contact surface between product and radiator. When the module is fastened on the surface of the radiator, the M5 or M6 screws and spring washers are used and fastened with 5NM torque. After the module is operated 1 hour, all screws must be refastened.
- 4) The connection with the main electrode of module can use copper, welding, socket and so on. The contact surface should be smooth and flat, which make good contact. While the connection with the control electrode of module is installed, attention should be paid to the corresponding connection of each pin. After the completion of the connection, do not plug and pull out the lead of the control electrode freely.

Ordering Information

JS

KD

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JieJie Semiconductor Co., Ltd

KD:Diode Module
 MD:Common Cathode Diode Module
 ND:Common Anode Diode Module

$I_{F(AV)}=120A$

12: $V_{RSM} \geq 1300V$
 16: $V_{RSM} \geq 1700V$
 18: $V_{RSM} \geq 1900V$
 20: $V_{RSM} \geq 2100V$

Performance Curves

FIG.1: Forward characteristics(per diode)

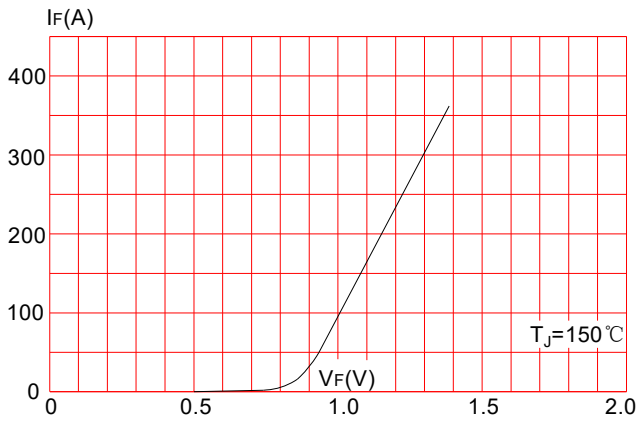


FIG.2: Peak on-state surge current

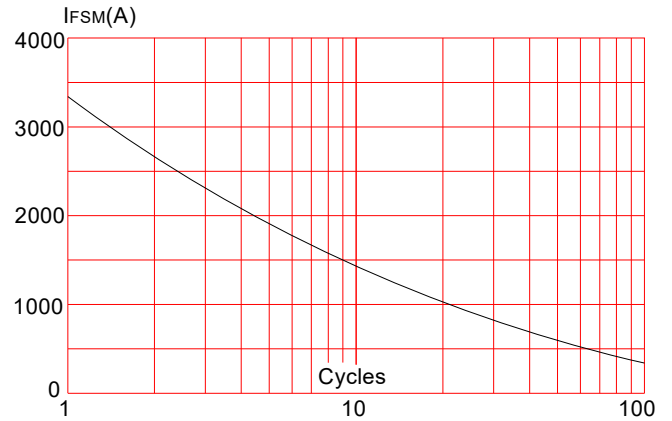


FIG.3: Forward current vs. case temperature

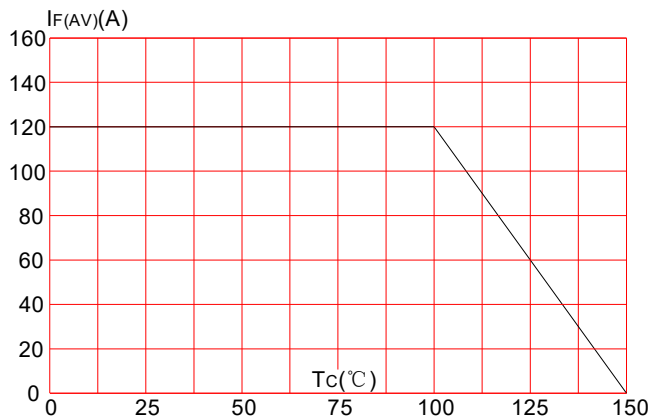
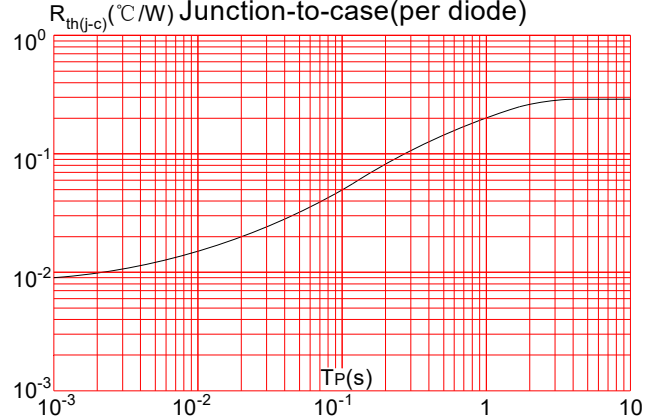


FIG.4: Maximum transient thermal impedance






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